rappresentazione grafica e tabella di verità delle porte logiche

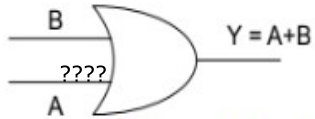
A	Y
0	1
1	0

Si legge: Y uguale A negato

A	B	Y
0	0	0
0	1	0
1	0	0
1	1	1

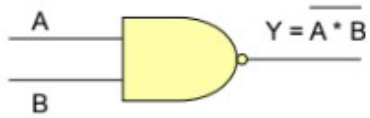
Si legge: Y uguale A AND B
o anche A per B

A	B	Y
0	0	0
0	1	1
1	0	1
1	1	1



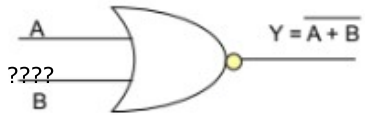
Si legge: Y uguale A OR B

A	B	Y
0	0	1
0	1	1
1	0	1
1	1	0

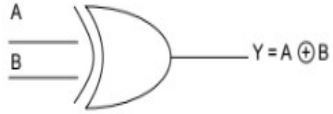


Si legge: Y uguale A AND B negato

A	B	Y
0	0	1
0	1	0
1	0	0
1	1	0



Si legge: Y uguale A OR B negato



A	B	Y
0	0	0
0	1	1
1	0	1
1	1	0

Si legge: Y uguale A OR ESCLUSIVO B